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HC 821 U.S. PRO  
09/746774

ISSUE CLASSIFICATION	
Class	Subclass
12/21/00	

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PATENT NUMBER

**U.S. UTILITY Patent Application**

 O.I.P.E. 	<b>PATENT DATE</b>  
SCANNED  Q.A. 	

APPLICATION NO.	CONT/PRIOR	CLASS 439	SUBCLASS 29	ART UNIT 2005	EXAMINER Chen, g
09/746774			846	3729	

## APPlicants

Harry Reijnders

## Process for contemporaneous manufacture and interconnection between adjoining printed wiring boards

PTO-2040  
12/98

## **ISSUING CLASSIFICATION**

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				<b>ISSUE FEE</b>	
	(Assistant Examiner)	(Date)		Amount Due	Date Paid
	(Primary Examiner)	(Date)		<b>ISSUE BATCH NUMBER</b>	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.					

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